

Maharshi Karve Stree Shikshan Samstha's
Cummins College of Engineering for Women, Karvenagar Pune – 411052
 (An Autonomous Institute Affiliated to SPPU)

T.Y.B.Tech: E&TC

Department of Electronics and Telecommunication Engg.

Academic Year: 2022-23

SEM-I

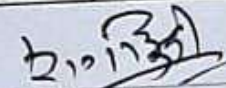
CLASS TIME-TABLE DIV:- A

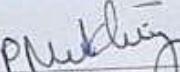
w.e.f. Monday, 08/08/2022


TIME/DAY	8-9	9-10	10-10.15	10.15-11.15	11.15-12.15	12.15 - 1.15	1.15 - 2.15	2.15 - 3.15	3.15 - 4.15	4.15 - 5.15
MON	DIP-AA1- (BVP) (ME LAB) DIP-AA2- (SPOT) (COMP-1) IoT-A- (SLS+SNICH) (TV LAB)		S H O R T B R E A K	MP-A1-(SGD) (PROJECT LAB) MP-A2-(SAM) (BEE LAB) DSP TUT-A3-(AMD) (CT LAB) 10.15-11.15 DSP TUT-A4-(AMD) (CT LAB) 11.15-12.15		L U N C H B R E A K	DIP-A (BVP) - (2) IoT-A+B (SLS) - (4) ITCT-A+B+C (SNO) - (4) (ME Tut Room)	DBMS-A -(ASP) (2) OS-A+B+C- (VAS) (4)	ED- (21-IT) - (WF) IDM-(14)-PD IPR-(8)-SAJ	PM-(10)-SS
TUE	AP-A1-(PPK)-(COMP-3) AP-A2-(PSI)-(FOC LAB) DSP-A3-(SNICH)-(COMP-4) VLSI-A4-(TL)-(COMP-2)			DSP-(10)(AMD)	AP-(10)(PPK)		DIP-A (BVP) - (2) IoT-A+B (SLS) - (4) ITCT-A+B+C (SNO) - (4) (ME Tut Room)	DBMS-A -(ASP) (2) OS-A+B+C- (VAS) (4)	SELF EXPRESSION	SELF EXPRESSION
WED	AP-A3-(PPK)-(COMP-3) AP-A4-(PSI)-(FOC LAB) DSP-A1-(SNICH)-(COMP-4) VLSI-A2-(TL)-(COMP-2)			MP-A3-(SGD) (PROJECT LAB) MP-A4-(SAM) (BEE LAB) DSP TUT-A1-(AMD) (CT LAB) 10.15-11.15 DSP TUT-A2-(AMD) (CT LAB) 11.15-12.15 ITCT-A-(SNO) (ME LAB)			DIP-A (BVP) - (2) IoT-A+B (SLS) - (4) ITCT-A+B+C (SNO) - (4) (ME Tut Room)	DBMS-A -(ASP) (2) OS-A+B+C- (VAS) (4)	ED- (21-IT)- WF IDM-(14)-PD IPR-(8)-SAJ	PM-(10)-SS
THU	AP-A1-(PPK)-(COMP-3) AP-A2-(PSI)-(FOC LAB) DSP-A4-(SNICH)-(COMP-4) VLSI-A3-(TL)-(COMP-2)			DSP-(10)(AMD)	AP-(10)(PPK)		VLSI-(10)(TL)	VLSI-(10)(TL)	DCCD-(2)-(SHR) SDS-(3)-(SRC)	PICD-(2)-(RTS) IDS-(3)-(MVP)
FRI	AP-A3-(GAYATRI)-(COMP-3) AP-A4-(PSI)-(FOC LAB) DSP-A2-(SNICH)-(COMP-4) VLSI-A1-(TL)-(COMP-2)			DSP-(10)(AMD)	VLSI-(10)(TL)		AP-(10)(PPK)	DSP TUT (5 students of ITCT) (With B2 Batch)	ED- (21-IT)-WF IDM-(14)-PD IPR-(8)-SAJ	PM-(10)-SS
SAT	DCCD-(2)-(SHR) SDS-(3)-(SRC)	DCCD-(2)-(SHR) SDS-(3)-(SRC)		DS LAB- (Batch-1)- (COMP-1)- (MVP) DS LAB- (Batch-2)- (COMP-4)- (SRC) DCCD LAB-(COMP-2)-(SHR+TL)			PICD-(2)-(RTS) IDS-(3)-(MVP)	PICD-(2)-(RTS) IDS-(3)-(MVP)	---	---


Name of Class Teacher: Dr.R.R.Borhade

Subject Name	Faculty Name	Subject Name	Faculty Name	Batches for Pract/Tuts only		Mentors
AP: Advanced Processor	PPK: Prof. P.P. Kamble	PM: Project Management	SS: Prof. Seema Shah	A1	3101-3120	T1 - Mrs.V.A.Sisale
DSP: Digital Signal Processin	AMD: Dr. A.M. Deshpande	IDM: Intro. to Digital Marketin	PD: Prof. Pranjali Deshpand	A2	3121-3140	T2 - Mr.S.L.Sahare
VLSI: VLSI Design	TL: Prof. Tanisha Londhe	ED: Entrepreneurship Dev.	WF: Teacher from Wadhwan	A3	3141-3160	T3- Dr.S.N.Ohatkar
DIP: Digital Image Processing	BVP: Dr. B.V. Pathak	IPR: Intellectual Propert Righ	SAJ: Dr. S. Jahirabadkar	A4	3161-3176	T4- Dr.R.R.Borhade
IoT: Internet of Things	SLS: Prof. S.L.Sahare	Self Expression				
ITCT: Info. Theory and	SNO: Dr. S.N. Ohatkar	DBMS: Database Mgmt. Syste	ASP: Prof. A.S. Patil			
MP: Mini Project	SGD: Prof. S.G. Dube	OS: Operating system	VAS: Prof. V.A. Sisale			
SDS: Stats for Data Science	SRC: Dr. S.R. Choudhary	IDS= Intro to Data Science	MVP: Prof. M.V. Pathade			
PICD= Physical IC Design	Prof.Ravi Suryavanshi	DCCD: Digital CMOS Ckt. Des	SHR: Dr.S.H.Rajput			


 Prof. S.G. Dube
 (Time Table In-Charge)


 Dr. P. Mukherji
 (H.O.D., E&Tc)


 Dr. Anand Bewoor
 (Dean, Academics)


 Dr. M. B. Khambete
 (Principal)

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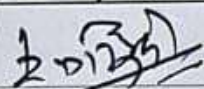
DIV:-B

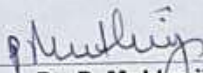
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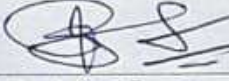
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MON	AP-(11)(NGP)	VLSI-(11)(SHR)	S H O R T B R E A K	AP-B1-(NGP)-(COMP-3) AP-B2-(GAYTRI)-(FOC LAB) DSP-B3-(SNAT)-(COMP-4) VLSI-B4-(TL)-(COMP-2)	L U N C H B R E A K	DIP-B+C (SPOT)- (3) IoT-A+B (SLS) - (4) ITCT-A+B+C (SNO) (ME Tut Room)	DBMS-B -(SAP) (3) OS-A+B+C- (VAS) (4)	ED- (21-IT) - (WF) IPR-(8)-(SAJ)	IDM-(9)-(SM) PM-(10)-(SS)	
TUE	DSP-(11)(SNAT)	AP-(11)(NGP)		MP-B1-(SGD) (PROJECT LAB) MP-B2-(SLS) (BEE LAB) DSP TUT-B3-(SNAT) (CT LAB) 10.15-11.15 DSP TUT-B4-(SNAT) (CT LAB) 11.15-		DIP-B+C (SPOT)- (3) IoT-A+B (SLS) - (4) ITCT-A+B+C (SNO) (ME Tut Room)	DBMS-B -(SAP) (3) OS-A+B+C- (VAS) (4)	SELF EXPRESSION	SELF EXPRESSION	
WED	VLSI-(11)(SHR)	DSP-(11)(SNAT)		AP-B3-(NGP)-(COMP-3) AP-B4-(GAYTRI)-(FOC LAB) DSP-B1-(SNAT)-(COMP-4) VLSI-B2-(SHR)-(COMP-2)		DIP-B+C (SPOT)- (3) IoT-A+B (SLS) - (4) ITCT-A+B+C (SNO) (ME Tut Room)	DBMS-B -(SAP) (3) OS-A+B+C- (VAS) (4)	ED- (21-IT) - (WF) IPR-(8)-(SAJ)	IDM-(9)-(SM) PM-(10)-(SS)	
THU	DSP-(11)(SNAT)	AP-(11)(NGP)		AP-B1-(MKP)-(COMP-3) AP-B2-(GAYTRI)-(FOC LAB) DSP-B4-(SNAT)-(COMP-4) VLSI-B3-(SHR)-(COMP-2)		DIP-BB1- (BVP) (ME LAB) DIP-BB2- (SPOT) (COMP-1) IoT-B- (SLS+SNICH) (POWER) (ITCT-B-(SNO) (TV LAB)	DCCD-(2)-(SHR) SDS-(3)-(SRC)	PICD-(2)-(SHR) IDS-(3)-(MVP)		
FRI	---	VLSI-(11)(SHR)		AP-B3-(NGP)-(COMP-3) AP-B4-(GAYTRI)-(FOC LAB) DSP-B2-(SNAT)-(COMP-4) VLSI-B1-(RTS)-(COMP-2)		MP-B3-(SGD) (PROJECT LAB) MP-B4-(SAM) (BEE LAB) DSP TUT-B1-(SNAT) (CT LAB) 1.15-2.15 DSP TUT-B2-(SNAT) (CT LAB) 2.15-3.15	ED- (21-IT) - (WF) IPR-(8)-(SAJ)	IDM-(9)-(SM) PM-(10)-(SS)		
SAT	DCCD-(2)-(SHR) SDS-(3)-(SRC)	DCCD-(2)-(SHR) SDS-(3)-(SRC)		DS LAB- (Batch-1)- (COMP-1)- (MVP) DS LAB- (Batch-2)- (COMP-4)- (SRC) DCCD LAB-(COMP-2)-(SHR+TL)		PICD-(2)-(RTS) IDS-(3)-(MVP)	PICD-(2)-(RTS) IDS-(3)-(MVP)	---	---	


Name of Class Teacher: Prof.S.A.Potdar

Subject Name	Faculty Name	Subject Name	Faculty Name	Batches for Pract/Tuts only	Mentors	
AP: Advanced Processor	NGP: Prof. N.G.Palan	PM: Project Management	SS: Prof. Seema Shah	B1	3201-3220	T1 - Dr.A.M.Deshpande
DSP: Digital Signal Process	SNAT: Prof. S. Natekar	IDM: Intro. to Digital Marketin	SM: Prof. Sakshi Mandke	B2	3221-3240	T2- Prof.M.S.Patankar
VLSI: VLSI Design	SHR: Prof. Seema Rajput	ED: Entrepreneurship Dev.	WF: Teacher from Wadhvani F	B3	3241-3260	T3-Prof.S.A.Potdar
DIP: Digital Image Processin	SPOT: Prof. Sandhya Potdar	IPR: Intellectual Propert Righ	SAJ: Dr. S. Jahirabadkar	B4	3261-3278	
IoT: Internet of Things	SLS: Prof. S.L.SahareSNICH	Self Expression				
ITCT: Info. Theory and	SNO: Dr. S.N. Ohatkar	DBMS:Database Mgmt. Syste	SAP: Prof. S.A. Paranjape			
MP: Mini Project	SGD: Prof. S.G. DubeS	OS:Operating system	VAS: Prof. V.A. Sisale			
SDS:Stats for Data Science	SRC: Dr. S.R. Choudhary	IDS= Intro to Data Science	MVP:Prof. M.V. Pathade			
PICD= Physical IC Design	Prof.Ravi Suryavanshi	DCCD:Digital CMOS Ckt. Des	SHR:Dr.S.H.Rajput			


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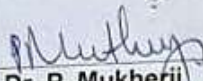
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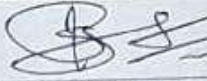
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MON	AP-C1-(MKP)-(COMP-3) AP-C2-(GAYTRI)-(FOC LAB) DSP-C3-(KSJ)-(COMP-4) VLSI-C4-(RTS)-(COMP-2)		S H O R T B R E A K	DSP-(10)(ARK)	---	L U N C H B R E A K	DIP-B+C (SPOT)- (3) IoT-C (SNICH) -(5) ITCT-A+B+C (SNO) -(ME Tut Room)	DBMS-C -(MP) (5) OS-A+B+C- (VAS) (4)	PM-(10)-(SS) IDM-(14)-PD IPR-(8)-(SAJ)	ED-(2-Instru) (VH)
TUE	DSP-(10)(ARK)	AP-(10)(MKP)		AP-C3-(MKP)-(COMP-3) AP-C4-(GAYTRI+PSI)-(FOC LAB) DSP-C1-(KSJ)-(COMP-4) VLSI-C2-(RTS)-(COMP-2)			DIP-B+C (SPOT)- (3) IoT-C (SNICH) -(5) ITCT-A+B+C (SNO)- (ME Tut Room)	DBMS-C -(MP) (5) OS-A+B+C- (VAS) (4)	SELF EXPRESSION	SELF EXPRESSION
WED	VLSI-(10)(RTS)	DSP-(10)(ARK)		DIP-C- (SPOT) (COMP-1) IoT-CC1- (SLS) (TV LAB) IoT-CC2- (SNICH) (POWER) ITCT-C-(SNO) (ME LAB)			DIP-B+C (SPOT)- (3) IoT-C (SNICH) -(5) ITCT-A+B+C (SNO) (ME Tut Room)	DBMS-C -(MP) (5) OS-A+B+C- (VAS) (4)	PM-(10)-(SS) IDM-(14)-PD IPR-(8)-(SAJ)	ED-(2-Instru) (VH)
THU	AP-(10)(MKP)	VLSI-(10)(RTS)		MP-C1-(SGD) (PROJECT LAB) MP-C2-(SAM) (BEE LAB) DSP TUT-C3-(ARK) (CT LAB) 10.15-11.15 DSP TUT-C4-(ARK) (CT LAB) 11.15-12.15			AP-C1-(MKP)-(COMP-3) AP-C2-(GAYTRI)-(FOC LAB) DSP-C4-(KSJ)-(COMP-4) VLSI-C3-(RTS)-(COMP-2)		DCCD-(2)- (SHR) SDS-(3)-(SRC)	PICD-(2)-(SHR) IDS-(3)-(MVP)
FRI	VLSI-(10)(RTS)	AP-(10)(MKP)		MP-C3-(SGD) (PROJECT LAB) MP-C4-(SLS) (BEE LAB) DSP TUT-C1-(ARK) (CT LAB) 10.15-11.15 DSP TUT-C2-(ARK) (CT LAB) 11.15-12.15			AP-C3-(MKP)-(COMP-3) AP-C4-(GAYTRI)-(FOC LAB) DSP-C2-(KSJ)-(COMP-4) VLSI-C1-(RTS)-(COMP-2)		PM-(10)-(SS) IDM-(14)-PD IPR-(8)-(SAJ)	ED-(2-Instru) (VH)
SAT	DCCD-(2)- (SHR) SDS-(3)-(SRC)	DCCD-(2)- (SHR) SDS-(3)-(SRC)		DS LAB- (Batch-1)- (COMP-1)- (MVP) DS LAB- (Batch-2)- (COMP-4)- (SRC) DCCD LAB-(COMP-2)-(SHR+TL)			PICD-(2)-(RTS) IDS-(3)-(MVP)	PICD-(2)-(RTS) IDS-(3)-(MVP)	---	---


Name of Class Teacher: Prof.M.K.Pote

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DSP: Digital Signal Process	ARK: Prof. A.R. Khedkar	IDM: Intro. to Digital Market	PD: Prof. Pranjali Deshpande	C2	3821-3840	T2-Dr.S.S.Musale
VLSI: VLSI Design	RTS: Prof. Ravi	ED: Entrepreneurship Dev.	VH: Dr. Vikas Hajare	C3	3841-3860	T3-Dr.S.A.Mangale
DIP: Digital Image Processin	SPOT: Prof. Sandhya Potda	IPR: Intellectual Propert Right	SAJ: Dr. S. Jahirabadkar	C4	3861-3877	
IoT: Internet of Things	SLS: Prof. S.L.Sahare	SNICH: Self Expression				
ITCT: Info. Theory and	SNO: Dr. S.N. Ohatkar	DBMS:Database Mgmt. System	MP: Prof. Madhura Paranjape			
MP: Mini Project	SGD: Prof. S.G. Dube	SAM: FOS:Operating system	VAS: Prof. V.A. Sisale			
SDS:Stats for Data Science	SRC: Dr. S.R. Choudhary	IDS= Intro to Data Science	MVP:Prof. M.V. Pathade			
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